APPLICATION

GMLB chip beads can be used in a variety of electronic applications including:

- Computers and Computer Peripherals
- Cellular Communication Equipment
- Digital Cameras
- Digital Televisions
- Audio Equipment

FEATURES

The GMLB Series is Mag.Layers' line of high quality ferrite chip beads. Using the latest in multilayer technology, we have developed chip beads that are able to resolve all EMI/EMC issues. High quality, reliability, and versatility make the GMLB series chip beads suitable for all your design needs.

• High Reliability

The monolithic inorganic materials used to construct GMLB chips restrain magnetic flux leakage thereby minimizing EMI concerns. GMLB chips are also extremely effective with unstable grounding.

- Small Chip-Shaped Design The chip-shaped design makes GMLB chip beads ideal for automatic mounting.
- High Soldering Heat Resistance High quality termination allows both flow and re-flow soldering methods to be applied.
- Sharp High Frequency Characteristics The GMLB high frequency chip series has sharp impedance characteristics, which make it suitable for high-speed signal lines.

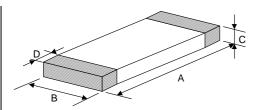
PRODUCT IDENTIFICATION

<u>GMLB</u> -	<u>100505</u> -	0030	<u>A</u> -	<u>N 8</u>	
1	2	3	4	5	6
 Product C 	ode				
② Dimensior	n Code				

- ③ Impedance (at 100 MHz)
- ④ Series Type
- ⑤ Design Code
- © Code for Special Specification



PRODUCT DIMENSION

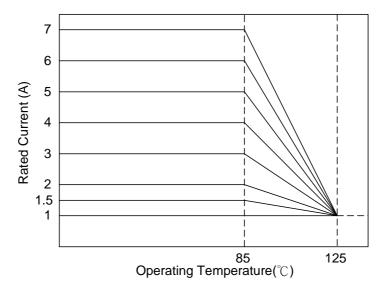


NOTE : Dimensions in mm

PRODUCT NO.	А	В	С	D
GMLB-100505	1.0±0.10	0.5±0.10	0.5±0.10	0.25±0.10
(0402)	(0.039±0.004)	(0.019±0.004)	(0.019±0.004)	(0.0095±0.004)

CURRENT DERATING

In operating temperatures exceeding $+85^{\circ}$ C, derating of current is necessary for chip ferrite beads for which rated current is 1.5A or over. Please apply the derating curve shown below according to the operating temperature.





ELECTRICAL REQUIREMENTS

Part Number	Impedance (Ω) at 100 MHz	R _{DC} (Ω) Max.	I _{DC} (mA) Max.	Operating Temp. Range (℃)
GMLB-100505-0120W-N8	120±25%	0.70	300	
GMLB-100505-0220W-N8	220±25%	1.00	250	-55 ~ +125
GMLB-100505-0600W-N8	600±25%	0.85	300	-55 ~ +125
GMLB-100505-1000W-N8	1000±25%	1.20	250	

 Temperature rise should be less than 40°C for P-type and less than 25°C for other types when rated current is applied.

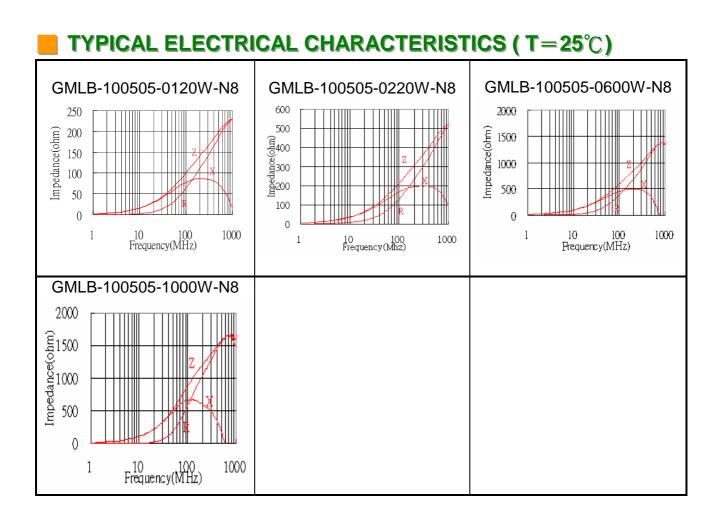
MEASURING METHOD / CONDITION

- •Test Instrument:
 - Z: Agilent 4291B Impedance Analyzer, Test Fixture: Agilent 16192 Osc. Level: 500mV

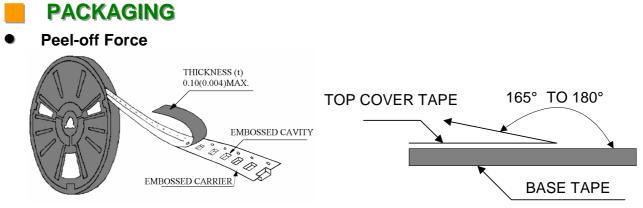
R_{DC}: Agilent 34401A

- Test Condition:
 - < Unless otherwise specified > Temperature: 15℃ to 35℃ Humidity: 25% to 85% RH
 - < In case of doubt > Temperature: 25℃ ± 2℃
 - Humidity: 60% to 70% RH

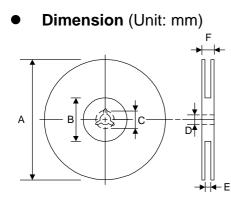




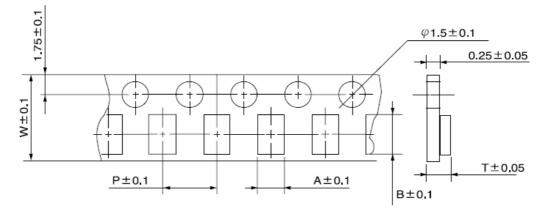




The force for peeling off cover tape is 10 grams in the arrow direction.

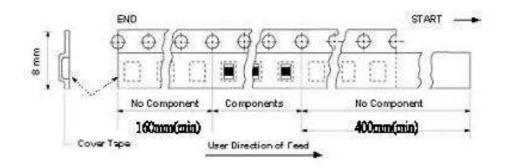


TYPE	A	В	С	D	E	F
8 mm	178±1	60 +0.5 -0	-	13 ±0.2	9 ±0.5	12 ±0.5
12 mm	178±0.3	60 ±0.2	19.3 ±0.1	13.5 ±0.1	13.6 ±0.1	-



TYPE	SIZE	А	В	W	Р	Т	CHIPS/REEL
GMLB	100505	0.6	1.1	8	2	1.0	10000

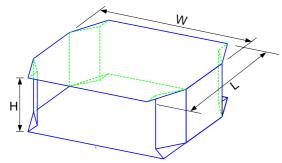




Taping Quantity

SERIES	1005	
PCS/Reel	10000	

• Tape Packing Case

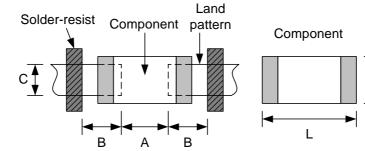


No. of Reels	W	L	н
2	18±0.5	18±0.5	2.4±0.2
3	18±0.5	18±0.5	3.6±0.2
4	18±0.5	18±0.5	4.8±0.2
5	18±0.5	18±0.5	6.0±0.2

W

Unit: cm

RECOMMENDED PCB LAYOUT



Unit: mm

Туре		1005		
Size	L	1.0		
	W	0.5		
A		0.45~0.55		
В		0.40~0.50		
С		0.40~0.50		

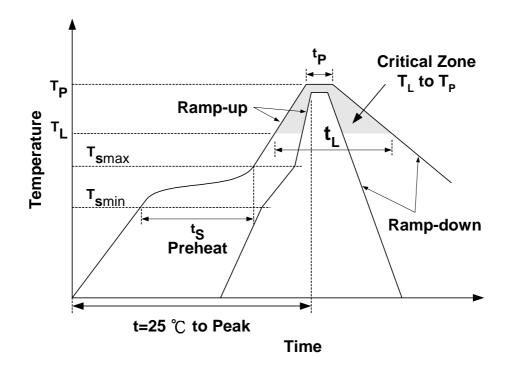


RELIABILTY TEST

 Mechanical Perform 	mance Test			
ITEM	SPECIFICATION	TEST		N
Solderability	More than 90% of the terminal electrode shall be covered with fresh solder.	Solder: 96.5Sn-3.0Ag-0.5Cu Solder Temperature: $245 \pm 5^{\circ}$ C Flux: Rosin Dip Time: 3 ± 1 Seconds		
Soldering Heat Resistance	The chip shall not crack. More than 75% of the terminal electrode shall be covered with solder.	Solder: 96.5Sn-3.0Ag-0.5Cu		
	The terminal electrode shall not be broken off	TYPE	W(KGF)	TIME (SEC)
Terminal Strength	nor the ferrite damaged.	GMLB-100505	0.2	30±5
	No mechanical damage.	TYPE	A(MM)	P(KGF)
Bending Strength	The ferrite shall not be damaged. $R_{0.5} \rightarrow 1.0$ Chip P Chip	GMLB-100505	0.4	0.2
Bending Test	Appearance: No damage Pressure jig	Substrate: PCB(100mm×40mm×1.6mm) Solder: Reflow Speed of Applying Force: 0.5mm / s Deflection: 2mm Hold Duration: 30 s		
Vibration	Impedance shall be within \pm 20% of the initial value. There shall be no mechanical damage.	The sample shall be soldered onto the printer circuit board and when a vibration having a amplitude of 1.52mm and a frequency of from 10 to 55Hz/1 minute repeated should b applied to the 3 directions (X,Y,Z) for 2 hour each.		
Drop shock	No apparent damage	Dropped onto p 100cm height three The terminals shall	e times in x	, y, z directions.
 Climatic test 				
ITEM	SPECIFICATION	TEST		N
Thermal Shock (Temperature Cycle)	Impedance shall be within \pm 20% of the initial value.			
Humidity Resistance		Humidity: 95% RH Time: 1000 \pm 12 H Temperature : 85°(ours	
High Temperature Resistance		Time: 1000 ± 12 H		
Low Temperature Resistance		Temperature : -40° Time: 1000 ± 12 H	°C ±2 °C	
-	ange: -55 $^{\circ}$ C TO +125 $^{\circ}$ C temperature should be within -40 $^{\circ}$ C ~85 $^{\circ}$ C a used within 6 months from the time of delive	•	d be less th	an 75% RH.



RECOMMENDED REFLOW SOLDERING PROFILE



Profile Feature		Sn-Pb	Pb-Free
	t _s	60~120 seconds	60~180 seconds
Preheat	T _{smin}	100 ℃	150 ℃
	T _{smax}	150 ℃	200 ℃
Average ramp-up rate (T _{smax} to T _P)		3℃/second max.	3℃/second max.
Time main above	Temperature (T _L)	183 ℃	217 ℃
Time main above	Time (t _L)	60~150 seconds	60~150 seconds
Peak temperature	(T _P)	230 ℃	250~260 ℃
Time within 5° \mathbb{C} of actual peak temperature (t _P)		10 seconds	10 seconds
Ramp-down rate		6°C/sec max.	6°C/sec max.
Time 25 $^\circ\!\!\mathbb{C}$ to peak temperature		6 minutes max.	8 minutes max.

NOTES

The contents of this data sheet are subject to change without notice. Please confirm the specifications and delivery conditions when placing your order.

