Application

The product is suitable for power line choke because of its excellent direct current characteristics

- PC/ Notebook
- PDA
- Digital camera
- DVD
- Cell phone
- Voltage conversion
- Low frequency filter circuit
- Choke
- Resonance circuits

Features

- The GMPA series is magnetically shielded chip based on multilayer process.
- New magnetic material is developed to get excellent direct current characteristics. This series has larger rated current than conventional GMLI series.
- Low DC resistance is realized.
- The cross talk characteristics are excellent because of the magnetically shielded structure.
- Compact size and lightweight.



Product Identification

GMPA - <u>252010</u> - <u>2R2 M S</u>

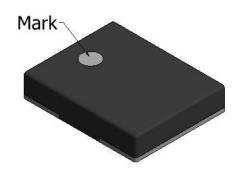
(1)

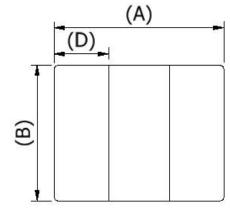
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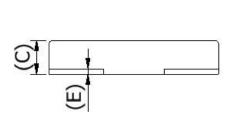
3 4 5

- ①: Product Code
- ②: Dimension Code (mm)
- ③: Inductance
- 4 : Tolerance Code :N = $\pm 30\%$,M = $\pm 20\%$,K = $\pm 10\%$
- ⑤: Code for Special Specification

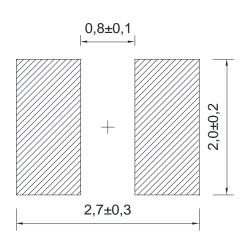
Product Dimension

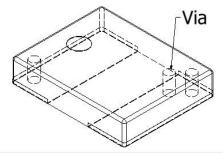






Recommended Solder Pad





(Unit: mm)

Α	В	С	D	E
2.5 ± 0.2	2.0 ± 0.2	1.0 max.	0.8 ± 0.2	0.1 max.



Electrical Characteristics

Inductance (μH) @ 1MHz	RDC (Ω)	Saturation Current*	Rated Current**
2.2μH ± 20%	0.12Ω Max.	550mA@typ.	1500mA@typ.

^{*} Inductance change should be less than ±30% when rated current is applied.

Test Conditions

Unless otherwise specified, the measuring conditions temperature shall be $5^{\sim}35^{\circ}$ C, the relative humidity RH shall be $45^{\sim}85\%$.

Electrical Characteristics Measuring Condition

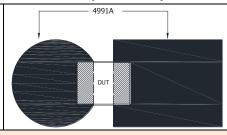
Impedance

Equipment: Agilent E4991A + 16197A Test Fixture or equivalent system

Inductance

Set the OSC 0.5mA @ 1MHz.

Place the DUT on test fixture and measure Ls value.



IDC

Equipment: Agilent 4285A + 42841A or equivalent system

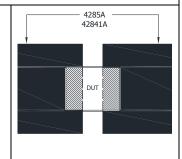
Set the OSC 0.5mA @ 1MHz.

Measure the initial inductance in the above circuit when IDC=0.

Rated current will be determined by the current which makes the inductance change to 30% lower than the initial inductance.

Set the test chip in a close chamber to avoid the effect of air flow.

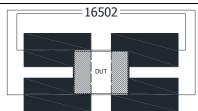
Measure the temperature on the surface of chip at current OA. Increase applied current step by step and measure the value when the temperature is stable.



DC Resistance

Equipment: Chroma 16502 or equivalent system

Place the sample in the test fixture then measure the value.



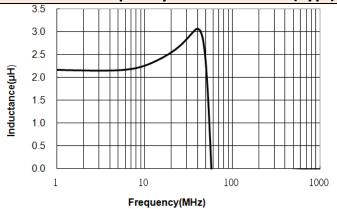


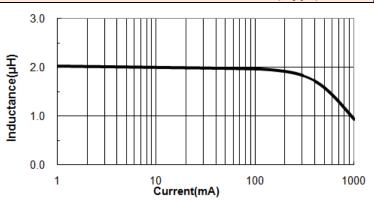
^{**}Temperature rise should be less than 40° C.

Characteristics Graph

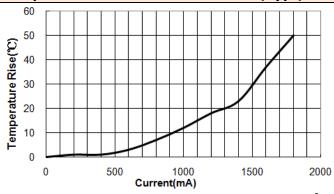
Inductance-Frequency Characteristics (Typ.)

Inductance-Current Characteristics (Typ.)





Temperature Rise Characteristics (Typ.)



The above tests are carried out under 25° C $\pm 5^{\circ}$ C ambient temperature.



Operating Temperature Range

-40°C to +125°C

Storage Condition

To maintain good solder ability of chips, care must be taken to control temperature and humidity in the storage environment.

Recommend condition:

Ambient temperature shall be at or under 40° C and keeping the humidity RH at or below 70%.

The products shall be stored in a place isolated from harmful gas like sulfur or chlorine.

The products shall be used within 6 months from the time of delivery. If the period is exceeded, please check solder ability before using the chips.

Green Products

This product meets green environmental protection rules on RoHS. RoHS compliance/HF free and EU Directive 2011/65/EU



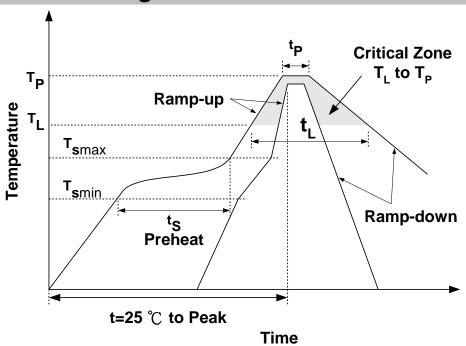
Reliability T	est	
Item	Specification	Test Condition
High Temperature	Inductance change to be	1000 hrs@ 125°C. Unpowered.
Exposure(Storage)	within 20% to the initial value.	Measurement at 24±4 hours after test conclusion.
Temperature	Inductance change to be	1000 cycles (-40°C to +125°C) Measurement at 24±4 hours
Cycling	within 20% to the initial	after test conclusion.
	value.	30min maximum dwell time at each temperature extreme.
		1 min. maximum transition time.
Biased Humidity	Inductance change to be	1000 hours 85°C/85%RH. Unpowered.
	within 20% to the initial value.	Measurement at 24±4 hours after test conclusion.
Resistance to	No apparent damage	Note: It is applicable to marked and/or coated components.
Solvents		Add Aqueous wash chemical OKEMCLEAN (A 6% concentrated Oakite cleaner) or equivalent. Do not use banned solvents.
Mechanical Shock	Inductance change to be	peak acceleration : 100 g's
	within 20% to the initial	Duration of pulse: 6 ms
	value.	Waveform : Half-sine
		Velocity change : 12.3 ft/sec
		Direction: X, Y, Z (3axes/3 times)
Vibration	Inductance change to be	Frequency and Amplitude: 10-2000 Hz.
	within 20% to the initial value.	5g's for 20 minutes, 12 cycles each of 3 orientations.
Resistance to	The chip shall not crack.	Solder: Sn-3.0Ag-0.5Cu
Soldering Heat	More than 75% of the	Flux: Rosin
	terminal electrode shall	After pre-heat for 2~3minutes at 150° C ~ 180° C.
	be covered with solder.	Immerse the test sample into a methanol solvent of rosin. Dip the sample into a solder bath at 260±5°C for 10±1sec.
Solder Ability	More than 95% area of	Solder: Sn-3.0Ag-0.5Cu
·	terminal electrode shall be	Flux : Rosin
	covered with fresh solder	After pre-heat for 2~3minutes at 150° C ~ 180° C .
		Immerse the test sample into a methanol solvent of rosin. Dip the sample into a solder bath at 245±5°C for 3±1sec.



Item	Specification	Test Condition
Flammability		Burning stops within 10 seconds on a vertical specimen;
		Drips of particles allowed as long as they are not inflamed.
Bending Test	No apparent damage.	Substrate: PCB(100mm×40mm×1.6mm)
		Solder: Reflow
		Speed of Applying Force : 0.5mm / s
		Deflection: 2mm
		Hold Duration: 60 s
		Support Solder Chip Printed circuit board before testing 45±2 KKIU112-M
		Probe to exert bending force Radius 340 Printed circuit board under test Displacement
Terminal	The terminal electrode shall	Force of 1.8 Kg for 60±1 seconds.
Strength(SMD)	not be broken off nor the	radius 0,5 mm
G (, ,	ferrite damaged.	DUT wide thickness substrate press tool
Operational Life	Inductance change to be	1000 hrs. @ 105°C.
	within 20% to the initial	Measurement at 24±4 hours after test conclusion.
	value.	



Recommended Soldering Profiles

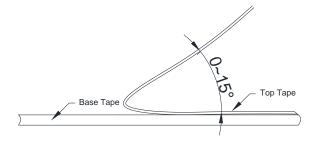


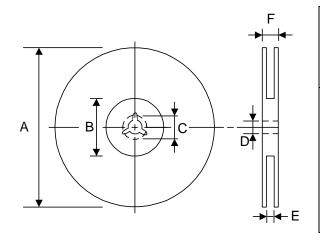
Profile Feature		Sn-Pb	Pb-Free
	t _s		60~180 seconds
Preheat	Preheat T _{smin}		150 ℃
	T_{smax}	150 ℃	200 °C
Average ramp-up rat	Average ramp-up rate (T _{smax} to T _P)		3°C/second max.
Temperature (T _L)		183℃	217 ℃
Time main above	Time (t _L)	60~150 seconds	60~150 seconds
Peak temperature (T _P)		230℃	250~260°C
Time within 5°C of actual peak temperature (t₁)		10 seconds	10 seconds
Ramp-down rate		6°C/sec max.	6°C/sec max.
Time 25℃ to peak temperature		6 minutes max.	8 minutes max.



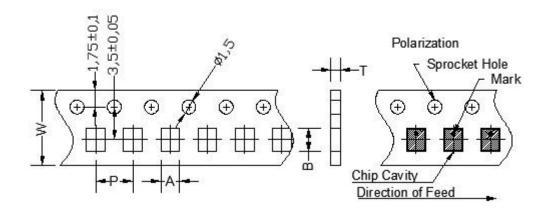
Tap Specification

The force for peeling off cover tape is 10 grams in the arrow direction.



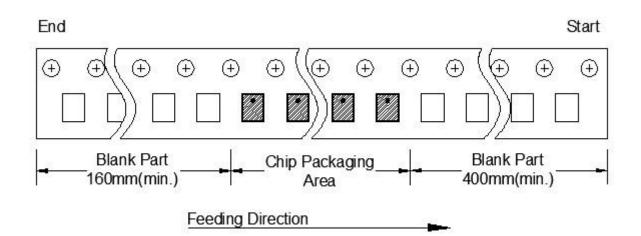


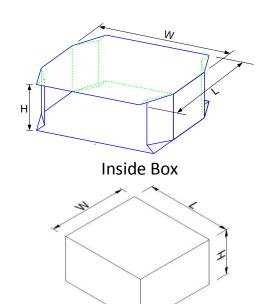
TYPE	A	В	С	D	E	F
8 mm	178±1	60±0.5	21±0.8	13±0.2	9±0.5	12±0.5



Α	В	W	Р	Т	Chips/Reel
2.4±0.1	2.75±0.1	8.0±0.2	4±0.1	0.95±0.15	4000

Packaging





Carton

No. of Reels	W (cm)	L (cm)	H (cm)	Chips/Box
3	18	18	3.6	12,000
5	18	18	6.0	20,000
No. of Box	W (cm)	L (cm)	H (cm)	Chips/Carton
2	14.6	19.2	19.8	40,000
5	14.6 34.7	19.2	19.8	40,000 100,000